

UPDATED ARM LBDS 2015 SEQUENCE

- B1: ARM LBDS 2015
 - B1: RESET BPMD
 - B1: ARM LHC BIC
 - RESET INTERLOCKED BPM
 - B1: ARM LHC BIC
 - SLEEP 2 S
 - B1: CHECK BIS EXCEPT LBDS CIBDS AND TCDQ-I
 - UNLINK PERMIT LOOPS B1B2
 - B1: PREPARE LBDS FOR ARMING 2015
 - ENABLE POSTMORTEM
 - CHECK LAST XPOC RESULT
 - B1: CHK LBDSK REMOTE AND ON
 - SET LBDS PROP INJANDDUMP=FALSE
 - ARM IPOC B1
 - B1: ARM THE BETS TCDQ
 - ARM BETS B1
 - ARM B1 LBDS
 - B1: WAIT TSU CLIENTS ARE OK
 - B1: ARM LBDS

The POP-UP window is removed, the ARM BETS-TCDQ task is removed, there is a new task that checks that all the BICs are armed except LBDS(PLC), CIBDS and TCDQ-BETS which are the tree interlocks that are left after ARM LHC BIC task.

The ARM BETS-TCDQ task is now un-skipped

Device	State	Mode	Control
BETS TCDQ-B1	OK	OPERATIONAL	REMOTE
BEI TCDQ	OK	OPERATIONAL	
BEI TCDQ2	OK	OPERATIONAL	
BEC BEC	OK	OPERATIONAL	
BETS TCDQ-B2	OK	OPERATIONAL	REMOTE
BEI TCDQ	OK	OPERATIONAL	
BEI TCDQ2	OK	OPERATIONAL	
BEC BEC	OK	OPERATIONAL	

If you get TCDQ-BETS tracking error, e.g. If you don't ramp the TCDQs, LHCOP has now the rights to do "Warning acknowledge" of the TCDQ-BETS in error.

The TCDQ-BETS Explorer can be found in CCM ==> LHC Control ==> Equipment Control ==> kickers ==> TCDQ BETS Explorer